



XC7K325T-2FFG900I Xilinx

XC7K325T-2FFG900I Xilinx is an ultra-high-performance FPGA chip in Xilinx's Kintex-7® FPGA family developed and designed by Xilinx State-of-the-art high-performance low power (HPL) 28nm high-k metal gate (HKMG) technology. It is currently one of the highest-grade chips. To purchase original and genuine high-end chips, please contact XT-ShenZhen@!!! You are welcomed to come to our factory to buy the hot sale and high-quality XC7K325T-2FFG900I Xilinx. We look forward to cooperating with you.

As a professional XT-ShenZhen@ XC7K325T-2FFG900I Xilinx manufacture, we would like to provide you XC7K325T-2FFG900I Xilinx. XC7K325T-2FFG900I Xilinx is an ultra-high-performance FPGA chip in Xilinx's Kintex-7® FPGA family developed and designed by Xilinx State-of-the-art high-performance low power (HPL) 28nm high-k metal gate (HKMG) technology. It is currently one of the highest-grade chips. To purchase original and genuine high-end chips, please contact XT-ShenZhen@!!!

Product description

The XC7K325T-2FFG900I Xilinx offers 28nm of optimal price/performance for your designs while providing you with high DSP ratios, a cost-effective package, and support for PCIe® Gen3 With 10 Gigabit Ethernet and other mainstream standards The performance of the new generation of FPGAs is more optimized than the previous generation It has up to 478K logic units. Programmable system integration to improve system performance and reduce BOM costs.

The DC and AC characteristics of the XC7K325T-2FFG900I are specified in commercial, extended, industrial, and military temperatures. All DC and AC electrical parameters are the same for a particular speed class, except for the operating temperature range. However, only selected speed classes or devices are available over each temperature range. All supply voltage and junction temperature specifications represent a worst-case scenario. The included parameters are common to popular designs and typical applications. Suitable for 3G and 4G wireless applications, flat panel displays, and IP video solutions

Product features

- . Total power reduction (50% lower than previous generation 40nm devices).
- . Accelerate design productivity, scalable optimized architecture, comprehensive tools with IP
- . State-of-the-art high-performance low power (HPL) 28nm high-k metal gate (HKMG) technology

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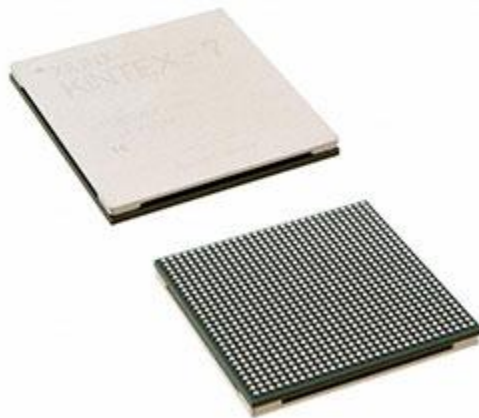
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- . Powerful clock management chip, combining a phase-locked loop with a mixed-mode clock manager module
- . Optional flip chip without cap and high-performance flip-chip package
- . The Kintex-7® FPGA provides high-speed serial connectivity via the built-in multi-gigabit transceiver
- . User-configurable analog interface (XADC), true 6-input lookup table (LUT) technology
- . High-performance SelectIO™ technology supports DDR3 interfaces up to 1866Mb/s

Product parameters

Technical parameters	The number of pins	900
	Number of inputs/outputs	400 Input
	Installation mode	Surface Mount
	encapsulation	FCBGA-900
	Operating temperature	-40°C-100°C (TJ)
	Product life cycle	Active
	RoHS standard	RoHS Compliant
	Leaded standard	Lead Free

Product images



FAQ

Q: Are you a trader or a manufacturer?

A: Yes, we are traders. XC7K325T-2FFG900I Xilinx we sell are purchased directly from the factory.

Q: What is your MOQ?

A: We will decide according to the product packaging method, and will communicate with you in time before placing an order.

Q: Are your shipments fast?

A: Yes, we have our own warehouse, and most of the goods are in stock. Same-day delivery can be achieved as soon as possible.

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